

Applicable Technologies Xilinx® FPGAs and Zynq® families Zynq® UltraScale+™ MPSoC Zynq®-7000 SoC	Requirements Work experience in the field of electronics	Contact Michael Schwarz P. +49 7664 91313-15 E. info@plc2.de
Fee (net per person) LT € 8,400	Inclusive Xilinx® evaluation board with example solutions Training material Beverages during breaks Lunch	Duration 8 dates of 2 days each

Workshop

PLC2 turns engineers and technicians into FPGA experts. Our three-month advanced training program imparts the know how for the entire Xilinx® embedded development process - on the job, alongside work. At the end of the training course, participants are able to develop designs for Xilinx® Zynq®-7000 SoC and Zynq® Ultrascale+™ MPSoC independently.

program consists of a coordinated combination of theory and practice. Participants learn to design and implement their own SoC/MPSoC designs. The training is divided into eight face to face units at PLC2 on-site, with two days each. Between the attendance sessions, there is the possibility to repeat and deepen the knowledge with training material and its own evaluation board.

The concept of our PLC2 Long Term Education is based on step by step training for participants. Starting with the architecture of the Zynq® SoC and MPSoC devices as well as the Vivado® tool flow, the underlying agenda includes the complete Xilinx® embedded development process. The course

Theoretical units are supplemented by exercises and own developments. Exercises and self-directed developments are a central part of the training. Of course, our trainers also provide support between the classroom sessions and are happy to answer questions.

Agenda

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| 01. Session 1:
Xilinx® embedded design flow | 05. Session 5:
Validation methodologies |
| 02. Session 2:
Processors, SoC, and MPSoC architectures | 06. Session 6:
Embedded systems integration |
| 03. Session 3:
AXI based IP development | 07. Session 7:
PetaLinux tools and development |
| 04. Session 4:
Embedded software design | 08. Session 8:
Linux drivers |