

Essentials of Microprocessors

Online Live

Workshop

Applicable Technologies

Xilinx® FPGAs, Zynq® SoCs/
MPSoCs

Requirements

Basic knowledge in VHDL
Basic knowledge of digital
design techniques

Contact

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Fee (net per person)

OL € 600

WO € 800

Inclusive

Training material

Plus beverages during breaks
Lunch

Duration

1 days

1 days

Workshop

Learn what makes microprocessors tick!
This class offers insights into all major aspects of microprocessors, from registers through coprocessors and everything in between. Differences between RISC and CISC architectures are explored as well as the concept of interrupts. A generic microprocessor is programmed and run in simulation to reinforce the principles learned in the lecture modules.

The attendee will leave the class well prepared for embedded classes like »Embedded System Design« or training classes around the Xilinx® Zynq® technology.

Due to accompanying exercises, the course offers in-depth and practice-oriented training. Attendees of the online live course will do the practical exercises in the afternoon on their own.

Agenda

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|--------------------------------------------|---------------------------------------|
| 01. Generic microprocessor architectures | 05. Interfacing with memory |
| 02. Generic assembly language | 06. Advanced microprocessor features |
| 03. Programming the generic microprocessor | 07. Processor architecture comparison |
| 04. Interrupt management and handler | |